

Performance Evaluation of Medium Voltage Resonant material based DC - DC Converter for Offshore Wind Farms

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Abstract. A resonating Zero-V, Zero-Cs DC-DC converter is anticipated in this broadsheet for medium voltage dc (MVDC) variety course of action of toward the ocean wind structures. The resounding converter is made out of bifull-interface cells sharing an augmentation leg, two transformers with disproportionate power assessments and dissimilar bounds, whose assistant windings are related in course of action and a voltage-doublers rectifier. The converter mechanisms in irregular current mode (DCM) and can accomplish zero-current exchanging (ZCS) for four rule power adjustments and rectifier diodes over the whole weight go. The two associate switches with minimal forward current can be turned on with ZVZCS, and slaughtered with Zero-voltage exchanging (ZVS). Subsequently, he trading incident can be contracted and capability is amended. The movement standard of the converter is poor down and proposal directions of guideline limits are presented. The effects of the turn's extent of aide transformer and deafening capacitance on the apex and killing streams are conversed in detail. Also, a model is worked in PLECS and re-enactment outcomes are given.

Keywords: MVDC collection system, resonant converter, zero-V zero-CS (ZVZCS), design rules, full bridge converter, various filters (capacitive, pi), different types of loads (R, RL, RLE).

1. Introduction

With the dispersal of petroleum products, issues, for example, ecological contamination become progressively unmistakable, which advance the improvement of sustainable power sources. Wind vitality –a mainstream sustainable power springs and consumes broadly abused in recent decades [1]. Because of steadier breeze profiles, a lot more grounded and progressively predictable breeze conditions, seaward wind homesteads can accomplish force among inland ones. Be that as it may, because of wind ranches are seaward; power transmission turns out to be progressively troublesome [2]. HV-Alternating Current and HV-Direct Current networks are two fundamental approaches to interface seaward twist homesteads to transmission framework and move the force. Considering power misfortunes and dependability of the transmission frameworks, HV-Direct Current lattices seem, by all accounts, to be an all the more encouraging innovation with development potential for applications in the association of seaward wind ranches [2]. Most of the seaward wind ranches utilize MV-Alternating Current (MVAC) assortment framework, which needs cumbersome MV step-up transformers and responsive influence remuneration gear [3],[4]. Hence, gigantic stages are expected to introduce massive transformers, which increment the expense and development trouble. medium voltage dc(MVDC)collection framework has been investigated thoroughly and is viewed as a promising innovation and the future improvement trend[3]-[10]due for its potential benefits, for example, lower vitality creation cost, simple to grow the seaward wind ranches power converter voltage evaluations and no limitation of turbine generator types [11-15]. Likewise on account of the use of medium-productivity transformers, the volume of converters can be little, lessening the development trouble of stage.

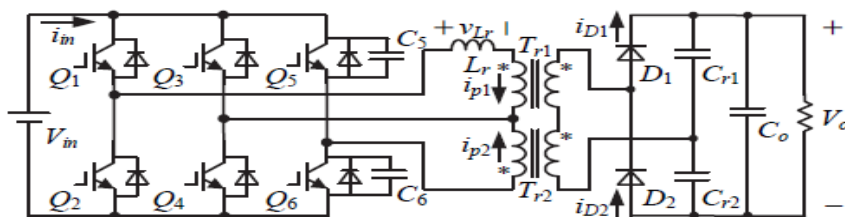


Figure 1. Proposed Converter

Late reports [15]-[21]. Stage move full extension (PSFB) converters have remained commonly used in elevation power use-case, inferable from that it can achieve Zero-VS with no additional unique contraptions. In any case, Zero-VS is lost under light-load ailments. A couple of methodologies is to mend light-load condition. A couple of strategies have been propped to improve light-load adequacy [22]-[49]. Single stage or three-stage double dynamic extension (DAB) converters are in like manner notable for that it can achieve Zero-VS over a wide action run. In MVDC switches with great voltage hassle are required in the MVDC side. Zero-CS is progressively helpful to diminish the state of mind executioner setback achieved by current tail than Zero-VS. A story dc-dc converter is proposed in for high-influence dc system, employing two full-interface converters in an exceptional way to recognize Zero-CS, at any rate eight IGBTs are used in the converter.

The proposed converter as shown in figure 1 and 2 involving two full framework cell. The central FB cell is made out of switches Q1~Q4, resonating inductor L_r and basic turning of essential transformer $Tr1$. The right hand FB cell is made out of switches Q3~Q6, primary bending of assistant transformer $Tr2$, and two Capacitors C_5 and C_6 which are related in comparing with Q5 and Q6. Switches Q3 and Q4 are shared by two F Bridge-cells. The rule FB-cell will deal with a large portion of force and the right hand F Bridge-cell will handle the remaining little circumstance of control. The discretionary bending of $Tr1$ and $Tr2$ are related in game plan as the commitment of a voltage - doubler rectifier, in which $Cr1$ and $Cr2$ are two full capacitors. C_o is the yield channel capacitor. The turn's extents of the discretionary to basic windings of $Tr1$ and $Tr2$ are $N1$ and $N2$ independently.

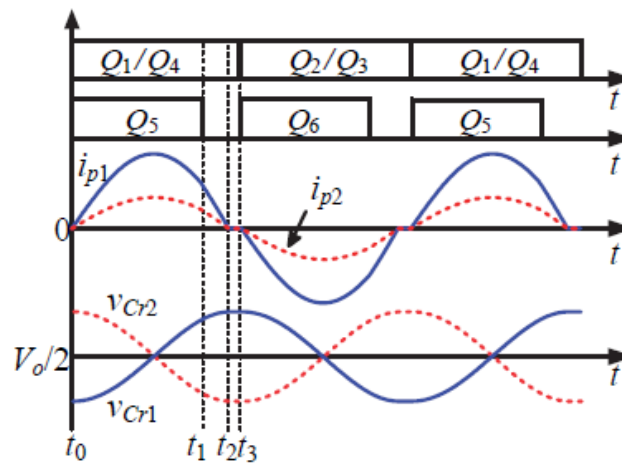


Figure 2. Vital Waveforms of the projected converter

2. Various modes of operation

2.1 Model [to, t1]:

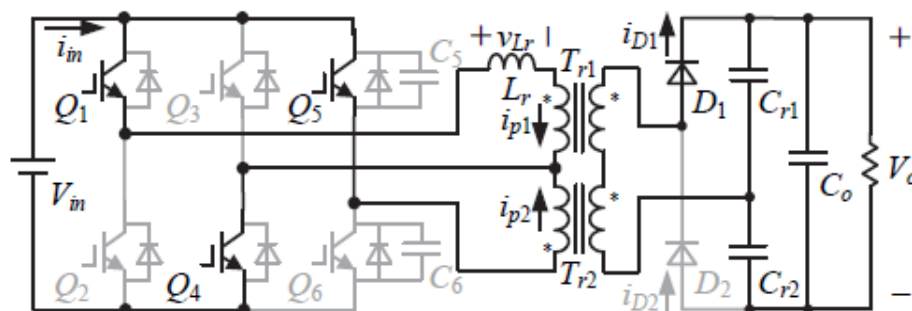


Figure 3. MODE1 [to, t1]

As shown in figure 3, switches Q1, Q4, Q5 and diode D1 are ON. The inductor L_r inaugurates to resonate with capacitor $Cr1$ and $Cr2$, and the voltage transversely L_r is $V_{in} - (V_{cr1} - N2V_{in})/N1 > 0$, foremost to the surge of currents i_{p1} and i_{p2} . Simultaneously, the voltage V_{cr1} keeps increasing while V_{cr2} decreasing.

2.2 Mode2 [t1, t2]:

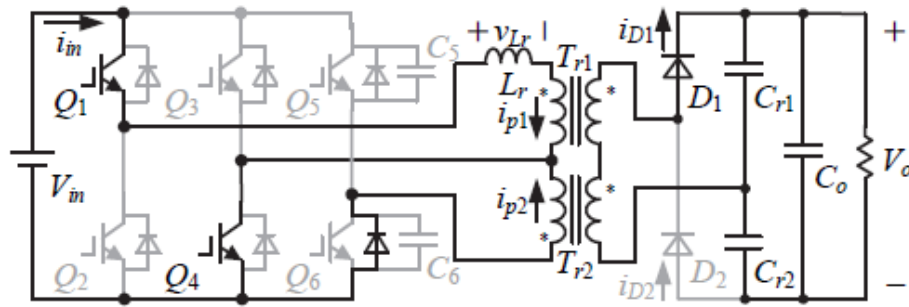


Figure 4. MODE2 [t1, t2]

As appeared in Figure4, Q5 is killed at t1. i_{p2} charges C5 and releases C6, Q5 is killed with ZVS since C5 and C6 limit the pace of ascent of the voltage across Q5. i_{p2} courses through Q4 and the counter equal diode of Q6, L_r despite everything reverberates with C_{r1} and C_{r2} . The voltage across L_r is $V_{in} - V_{cr1}/N1 < 0$, prompting the expire of i_{p1} and i_{p2} .

2.3 Mode2 [t1, t2]:

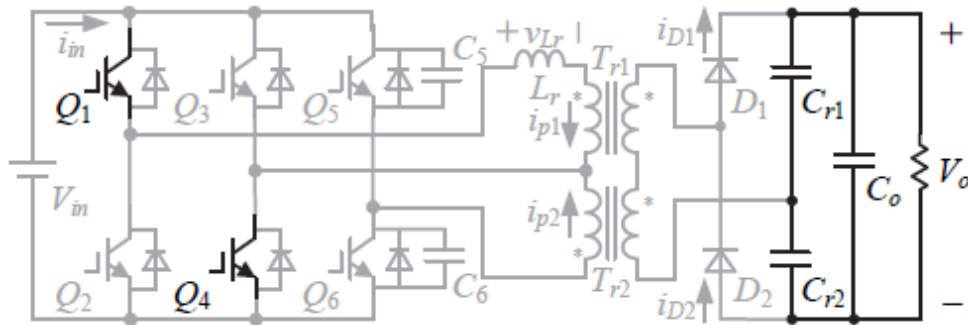


Figure 5. MODE3 [t2, t3]

As appeared in Figure 5, i_{p1} and i_{p2} tumble to zero till t2. The voltage across D1 is $N1V_{in} - V_{cr1} < 0$, and the voltage across D2 is $-N1V_{in} - V_{cr1} < 0$, so diode D1 and D2 are off. The LC reverberation stops because of L_r , C_{r1} , and C_{r2} are detached. Although switches Q1 and Q4 are still on, i_{p1} and i_{p2} preserve zero in this genre. The heap is fueled by the yield channels capacitor C_0 , voltage across C_{r1} and C_{r2} stays unaltered.

3. Simulation

3.1 Closed loop control of resonant Zero-V, Zero-CS DC-DC Converter with PI-Filter Using RL Load

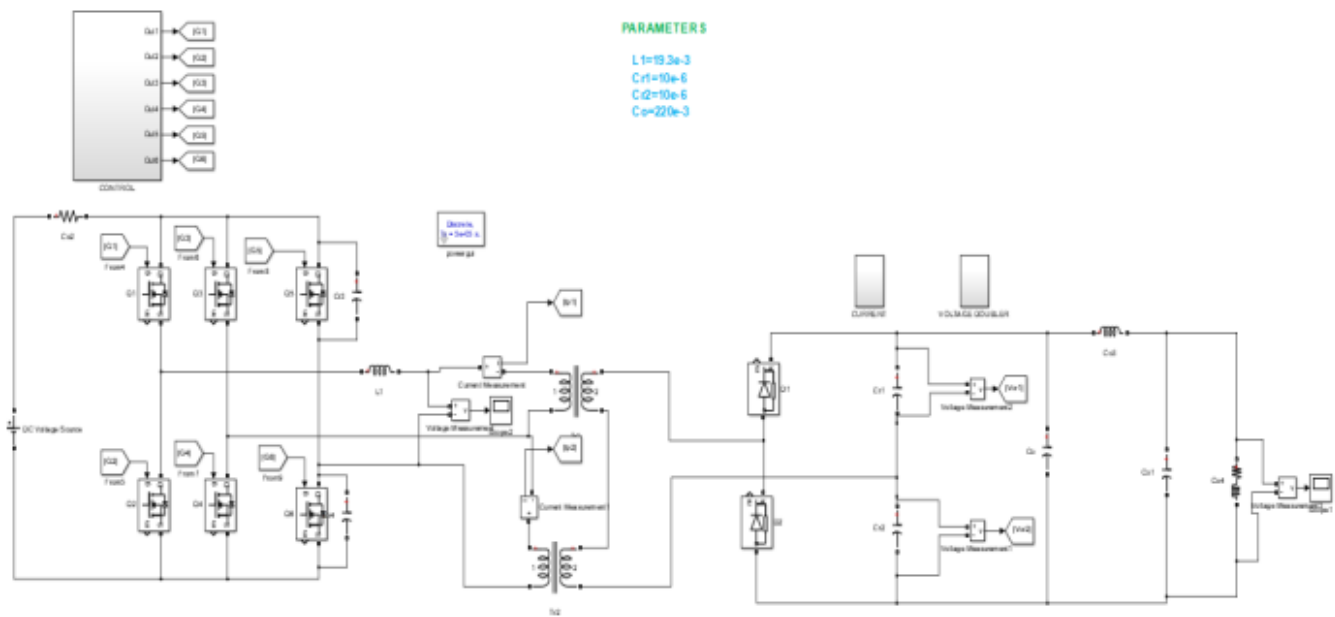


Figure 6. Closed loop control of resonant Zero-V, Zero-CS DC-DC Converter with PI-Filter Using RL Load

The circuit diagram of closed loop schematic of resonant Zero-V, Zero-CS DC-DC Converter with PI-Filter is presented in the figure 6.

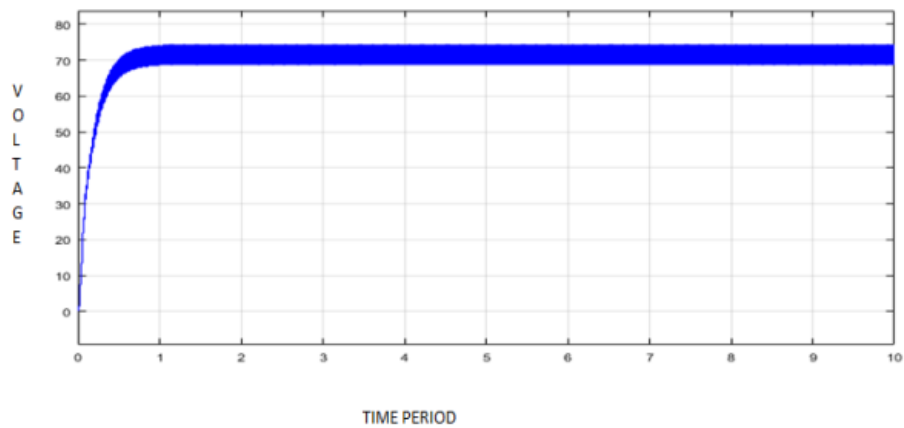


Figure 7. Output Voltage Waveform

The waveform of output voltage is shown in figure 7, it has an output voltage of 70v.

3.2 A Closed loop control of resonant Zero-V, Zero-CS DC Converter with C-Filter using R-Load.

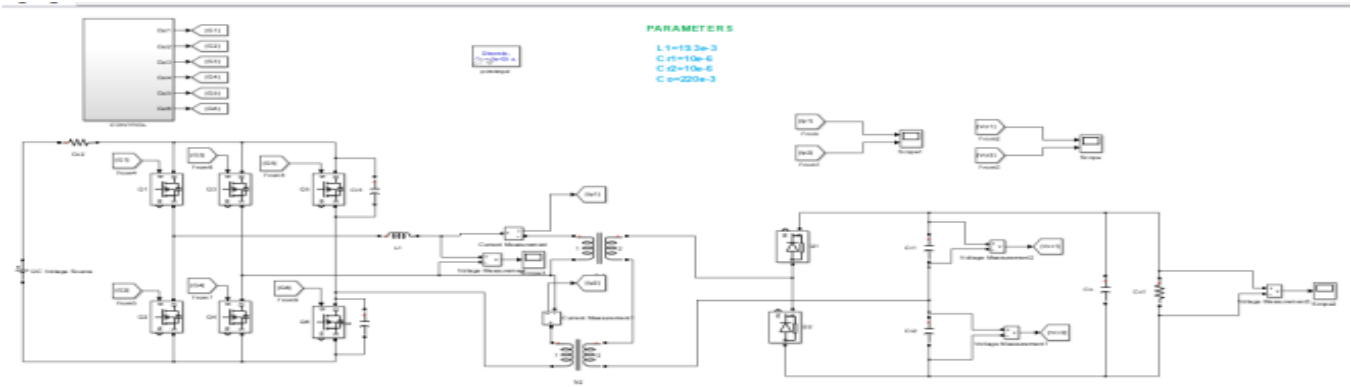


Figure 8. Closed loop control of resonant Zero-V, Zero-CS DC-DC Converter with Capacitive Filter Using R Load.

The circuit diagram of closed loop resonant Zero-V, Zero-CS DC - DC converter with capacitive filter is shown in the figure 8.

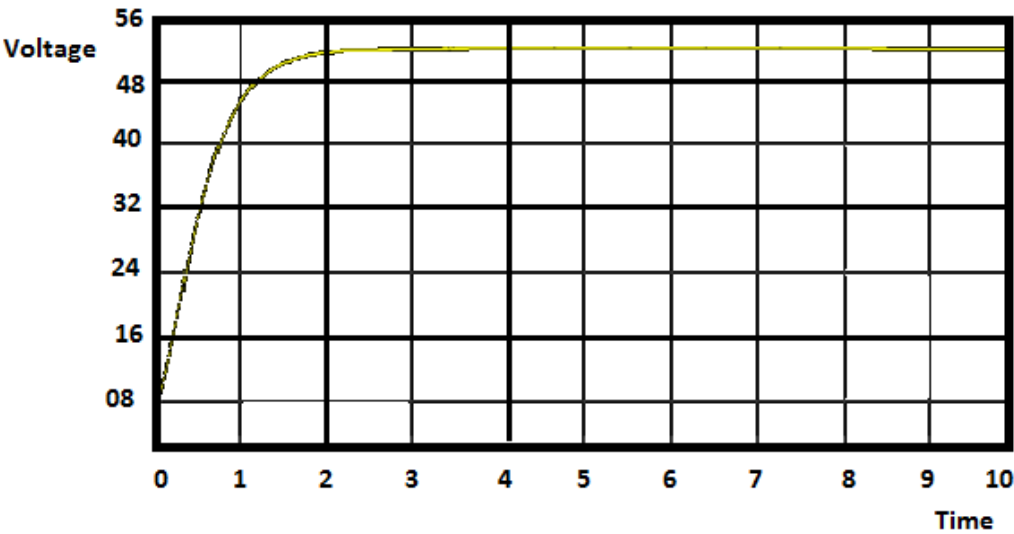


Figure 9. Output Voltage Waveform

The waveform of production voltage is shown in scheme 9, it has an output voltage of 54v.

3.3 A Closed Loop Control of Resonant Zero-V, Zero-CS DC-DC Converter With PI-Filter Using RLE-Load.

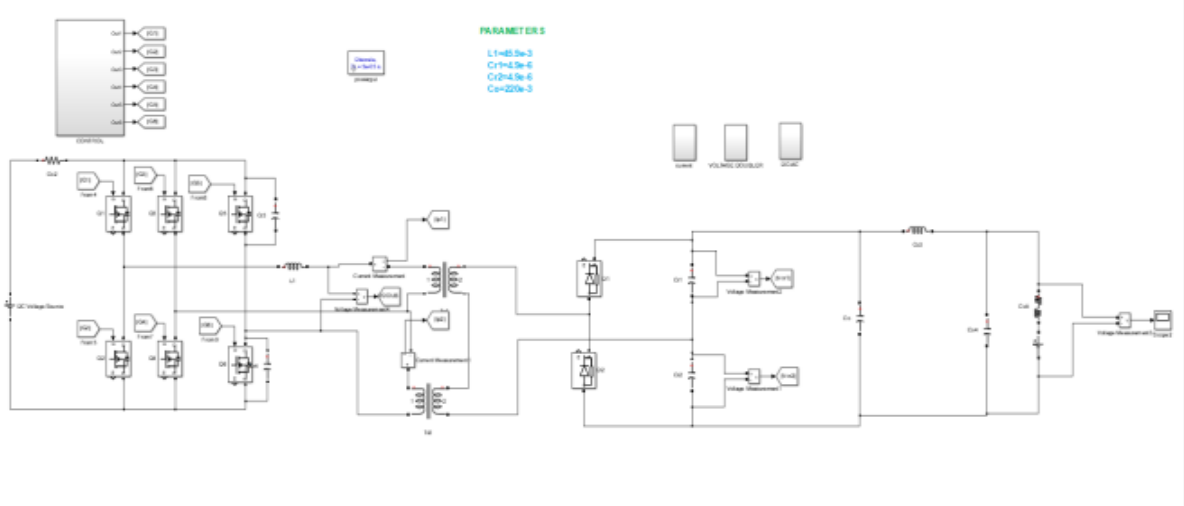


Figure 10. Closed loop control of resonant Zero-V, Zero-CS DC-DC Converter with PI-Filter using RLE Load

The circuit diagram of Closed Loop circuit diagram of resonant Zero-V, Zero-CS DC-DC Converter of with PI-Filter using RLE load is shown in the figure 10.

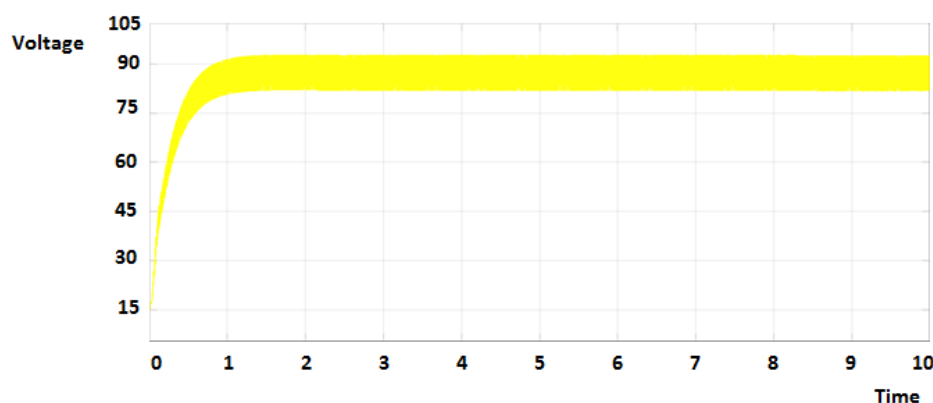


Figure 11. Output Voltage Waveform

The waveform of output voltage is shown in figure 11, it as an output voltage of 102v.

Table 1 Comparison of i/p voltage and o/p voltage, Voltage across capacitor with different loads values

TYPE	I/P(V)	O/P(V)	V Across(c)
PI(RL)	150V	70V	Vcr1=19000v Vcr2=19000v
C(R)	150V	54V	Vcr1=20000v Vcr2=20000v
PI(RLE)	150V	102V	Vcr1=32000v Vcr2=32000v

From table 1 we compare output voltage, input voltage, voltage across capacitor with different loads. It shows that PI-Filter using RLE-Load produces the output voltage of 102 V with 100% improvement while compare with other methods.

4. Conclusion

A MV full dc-dc converter for toward the ocean wind farm scattering mechanism. It encompasses four basic buttons, two right hand adjustments, guideline transformer and one partner transformer. Also, crucial switches and everything diodes can complete Zero-CS turn-on and turn-off, and partner shifts can achieve Zero-V, Zero-CS turn-on and Zero-VS turn-off, so great adequacy can be gotten. In addition, the converter makes V-rule complete clear fixed-repeat PWM.

References

- [1] L. Jiang, C. Wang, Y. Huang, Z. Pei, S. Xin, W. Wang, S. Ma, and T. Brown, "Growth in Wind and Sun: integrating variable generation in China," *IEEE Power and Energy Magazine*, Vol. 13, no. 6, pp. 40-49, Nov-Dec. 2015.
- [2] P. Bresesti, W. L. Kling, R. L. Hendriks, and R. Vailati, "HVDC connection of off shore wind farms to the transmission system," *IEEE Trans. Energy conversion*, Vol. 22, no. 1, pp. 37-43, Mar. 2007.
- [3] M. S. Carmell, F. Castelli-Dezza, D. Rosati, G. Marchegiani, and M. Mauri, "MVDC Connection of offshore wind farms to the transmission system," *SPEEDAM, Pisa*, 2010, pp. 1201-1206.
- [4] C. Zhan, C. Smith, A. Crane, A. Bullock, and D. Grieve, "DC Transmission and distribution system for a large offshore wind farm," *9th IET International conference on AC and DC Power Transmission*, ACDC, London, 2010, pp. 1-5.
- [5] Raja Krishnamoorthy, D. Kodandapani, Gowrishankar Kasilingam, N. B. Prakash, C. Bharatiraja, T. Jayasankar "Design of low-power coupled chopper instrumentation amplifier using pin pong ripple reduction for biomedical applications," *Materials Today Proceedings (Elsevier 2020)*, ISSN: 2214-7853, <https://doi.org/10.1016/j.matpr.2020.09.725>
- [6] W. J. Lee, C. E. Kim, G. W. Moon, and S. K. Han, "A new phase-shifted full-bridge converter with voltage-doubler-type rectifier for high-efficiency PDP Sustaining Power Module," *IEEE*

- Trans.Ind.electron.,Vol.55.no.6,pp.2450-2458,Jun.2008.
- [7] G.N.B.Yadav and N.L.Narasamma,"AN Active soft switched phase-shifted full-bridge DC –DC converter.analysis,Modeling,design and implementation,"IEEE Trans.Power Electron.,vol.29,no.9,pp.4538-4550,Sep.2014
 - [8] Y. S. Lai,Z.J.Su,and Y.T. Chang,"Novel Phase-Shifted control technique for full-Bridge Converter to Reduce thermal imbalance under light-load condition,"IEEE Trans.Ind.Electron.,vol.51,no.2,pp.1651-1659,Mar.2015.
 - [9] L.Zhao, H.Li, Y.Hou and Y.Yu,"Operation analysis of a phase-shifted Full bridge converter during the dead-time interval,"IET Power Electron.,Vol.9,no. 9,pp.1777-1783,Jul.2016.
 - [10]. G. G. Oggier and M. Ordonez,"High-efficiency DAB converter using switching sequence and burst mode,"IEEE trans.Power Electron.,Vol.31, no. 3,pp. 2069-2082,Mar.2016.
 - [11]. H.A.B.Siddique,S.M.Ali,and R.W.De Doncker,"DC collector grid configurations for large photovoltaic parks," 15th European Conference on Power Electronics and applications(EPE),Lille,2013,pp.1-10.
 - [12]. Y.Mei, S.Hu,L. Lin, W.Li,X. He, and F. Cao,"Highly efficient and reliable inverter concept based transformerless photovoltaic inverters with tri-direction clamping cell for leakage current elimination,"IET Power electron.,Vol. 9, no. 8,pp. 1675- 1683,jun.2016.
 - [13] X. Tan and X.Ruan,"Optimal design of DCM LCC resonant converter with inductive filer based on mode boundary map,"IEEE Trans Power Electron.,Vol.30,no.8,pp.4144-4155,Aug.2015.
 - [14] V.Belaguli and A.K S.Bhat,"High Power factor operation of DCM series-parallel resonant converter ,"IEEE Trans.Aersp.Electron.Syst.,vol.35,no.2,pp.602-613,Apr.1999 .
 - [15] K.A. Cho, S. H. Ahn,S. H. Ahn,S.B.ok,H.J.Ryoo,S.R.Jang,and G.H.Rim,"Design of LLC resonant converter for renewable energy systemwithwide-range input voltage,"Proc.IPEM,Harbin,2012- 1228.
 - [16] RajaKrishnamoorthy, T.Jayasankar, S.Shanthi,M.Kavitha,C.Bharatiraja, "Design and implementation of power efficient image compressor for WSN systems," *Materials Today Proceedings Elsevier* 2020),<https://doi.org/10.1016/j.matpr.2020.09.221>
 - [17] A. K. S. Bhat,"Analysis and design of a series-parallel resonant converter,"IEEE Trans.Power Electron., Vol. 29,no. 1,pp.1- 11,jan.1993.
 - [18] S. Shah and A. K. Upadhyay,"Analysis and design of a Series-parallel resonant converter operating in discontinuous conduction mode,"Proc.IEEE APEC,pp.165-174,mar.1990.
 - [19] V. Belaguli and A.K. S. Bhat,"series-parallel resonant converter operating in discontinuous current mode__analysis,design,simulation,and experimental results,"IEEE Trans. Circ. And sys.,vol.47,no.4,pp.433-442,apr2000.
 - [20] Z. Guo, K. Sun and D.Sha,"Circulating current reduced three level DC-Dc converter with two transformers for battery charges,"IEEE 8th international power electronics and Motion control conference (IPEMC-ECCE Asia), Hefei,2016,pp.78- 84.
 - [21] T. L abella, W. Yu, J. S. Lai, M. Senesky, and D.Anderson,"A bidirectional-switch-based wide- input range high efficiency isolated resonant converter for PV applications,"IEEE Trans.Power Electron.,Vol.29,no. 7,pp.3473-3484,jul.2014.
 - [22] V. Senthil Nayagam, G. T. Sundar Rajan and V. Balasubramanian, "Improved Power Factor at Input Stage of Pseudoboost Rectifier with Improved Switching Pattern", International Journal of Applied Engineering Research, Volume 10, Number 6, 2015, pp. 5158 – 5164.
 - [23] R.Hemaprithni and G.T.Sundar Rajan, "Three Level Integrated AC to DC Converter fed DC Drive with Cascaded filter", International Journal of Applied Engineering Research, Volume 10, Number 6, 2015, pp. 5140 – 5146.
 - [24] D. Jayanthi, G.T.Sundar Rajan, "A Novel Unity Power Factor At Input Stage Of Vienna Rectifier For Wind Energy Conversion System Using Fuzzy Logic", International Journal of Applied Engineering Research, Volume 10, Number 6, 2015, pp. 5650 – 5655.
 - [25] G. T. Sundar Rajan and C. Christoher Asir Rajan, "A Novel Unity Power Factor Input Stage With Resonant DC Link Inverter for AC Drives", Journal of Electrical Engineering, Volume 12 / 2012 - Edition: 4, pp. 62 – 66, 2012.
 - [26] G. T. Sundar Rajan and C. Christoher Asir Rajan, "Fuzzy Inference System Based Power Factor Correction of Three Phase Diode Rectifier using Field Programmable Gate Array", American Journal of Applied Sciences, Volume 10 - Issue 9 / 2013, pp. 986-999.
 - [27] G. T. Sundar Rajan, "Power Quality Improvement at Input and Output Stages of Three Phase Diode Rectifier using Artificial Intelligent Techniques for DC and AC Drive Applications", IEEE International Conference on Computational Intelligence and Computing Research (ICCIC - 2014, 2014), PARK College of Engineering and Tekhnology, Coimbatore, Tamilnadu, INDIA, pp. 904 – 909,

December 18 to 20.

- [28] G. T. Sundar Rajan and C. Christofer Asir Rajan, "Input stage improved power factor of three phase diode rectifier using hybrid unidirectional rectifier", International conference on Nanoscience, Engineering and Technology – ICONSET – 2011, Sathyabama University, pp. 697 – 682, November 28 to 30, Chennai.
- [29] G. T. Sundar Rajan, Barnabas Paul Gladly. J and A. Ramesh Babu, "Simulation and Implementation of Two-Inductor PFC Boost Converter Using Single Pole Double Throw Switch (SPDT) Relay", 2017 IEEE International Conference on Computational Intelligence and Computing Research.
- [30] A. Ramesh Babu, G. T. Sundar Rajan, J. Barnabas Paul Gladly and V. Geetha, "Multi-MOSFET controlled half bridge AC – AC converter for Induction heating applications", International Journal of Ambient Energy, 31 Jan 2019.
- [31] P. Velazhagan, A. Ramesh Babu, M. D. Vijayakumar and G. T. Sundar Rajan, "Prospect of Pico-Hydro Electric Power Generation Scheme by Using Consuming Water Distributed to Multi Storage Building", Lecture Notes in Electrical Engineering, Publisher: Springer Verlag.
- [32] A. Ramesh Babu, Raghavendiran, Barnabas Paul Gladly and G. T. Sundar Rajan, "Novel non- isolated high step-down three phase interleaved DC-DC converter with capacitor voltage division techniques", International Conference on Circuit ,Power and Computing Technologies (ICCPCT), 2017 20-21 April 2017.
- [33] V.Balasubramanian, Dr. Sivachidambaramanathan and G.T.Sundar Rajan, "Super Junction MOSFET And Silicon Carbide Diode Based Transformerless Solar Photo Voltaic Inverter Topology For High Power Conversion Efficiency, Zero Dead Time And Minimized Ground Leakage Current", International Journal of Applied Engineering Research, Volume 10, Number 6, 2015, pp. 5195 – 5199.
- [34] Catherine Amala Priya. E and G. T. Sundar Rajan, "Renewable energy source based Z source inverter interlinked to grid with Harmonics optimization techniques", IEEE International Conference on Computational Intelligence and Computing Research (ICCIC – 2018), Thiyagaraja College of Engineering and Tekhnology, Madurai, Tamilnadu, INDIA, pp. 150 – 155, December 13 to 15.
- [35] C. T. Manikandan and G. T. Sundar Rajan, "Performance analysis of solar based high step up DC – DC converter with various filters" IEEE International Conference on Computational Intelligence and Computing Research (ICCIC - 2018, 2014), Thiyagaraja College of Engineering and Tekhnology, Madurai, Tamilnadu, INDIA, pp. 141 – 145, December 13 to 15.
- [36] E.Catherine Amala Priya and G. T. Sundar Rajan, "An Improved Model of Hybrid Multi Converter used for Grid Connected Applications", International Journal of Power Electronics and Drive Systems (IJPEDS), Vol 10, No 2: June 2019.
- [37] Sundar Rajan G T, Barnabas Paul Gladly J and Ramesh Babu A, "Performance Evaluation of Interleaved Converter using Pulse Width Modulation (PWM) Technique for voltage regulation and power factor", Proceedings of the 2nd International Conference on Smart Systems and Inventive Technology, ICSSIT 2019.
- [38] Ramesh Babu A, Velumani M, Sundar Rajan G T and Sudarsan N, "Regulation of Dc link voltage for dynamic braking system for wind turbine PMSG", International Journal of Scientific and Technology Research (2020) 9(3) 6825-6830.
- [39] Ravikumar D and Sundarajan G T, "Fault tolerant inverter for high speed PMBLDC drive in aerospace applications", International Journal of Scientific & Technology Research Volume 9, Issue 02, PP 4393 – 4398, February 2020.
- [40] CT.Manikandan and G.T.Sundarrajan "Closed Loop control of Interleaved Boost DC-DC Converter fed DCMotor system using Fuzzy Logic Controller" , Journal of Xidian University, VOLUME 14, ISSUE 4, 2020 pp 2890 – 2904.
- [41] Babu, A. Ramesh, and T. A. Raghavendiran. "Analysis of non-isolated two phase interleaved high voltage gain boost converter for PV application." In Control, Instrumentation, Communication and Computational Technologies (ICCICCT), 2014 International Conference on, pp. 491-496. IEEE, 2014.
- [42] Babu, A. Ramesh, and T. A. Raghavendiran. "Performance analysis of novel three phase High step-up dc-dc interleaved boost converter using coupled inductor." In Circuit, Power and Computing Technologies (ICCPCT), 2015 International Conference on, pp. 1-8. IEEE, 2015.
- [43] Saravanan, M., and A. Ramesh Babu. "High Power Density Multi-Mosfet-Based Series Resonant Inverter for Induction Heating Applications." International Journal of Power Electronics and Drive Systems (IJPEDS) ISSN: 2088-8694, Vol. 7, No. 1 pp. 107-113, March 2016.
- [44] Babu, A. Ramesh, and T. A. Raghavendiran. "High voltage gain multiphase interleaved DC-DC converter for DC micro grid application using intelligent control." Computers & Electrical Engineering, ISSN:0045-

- 7906, Vol.74, pp.451-465, March 2019.
- [45] J Barnabas Paul Gladly, K Manjunath, “Analysis-intelligent design and simulation of improved state–space modelling for control of Luo converter”, Elsevier Computers & Electrical Engineering Vol.75, 230-244.
 - [46] J Barnabas Paul Gladly, K Manjunath, “Stability Analysis of third order DC to DC converter by considering input current as output vector”, International Journal of Engineering & Technology , vol.7 No.(3.20) (2018) 677-682.
 - [47] Sivachidambaranathan.V & S.S.Dash (2010), “Simulation of Half Bridge Series Resonant PFC DC to DC Converter”, IEEE International Conference on “Recent Advances in Space Technology Services & Climate Change – 2010” (RSTS&CC-2010), Sathyabama University in association with Indian Space Research Organisation (ISRO), Bangalore and IEEE, ISBN 978-1-4244-9184-1, November 13-15, IEEE Explore pp 146-148.
 - [48] Geetha.V and Sivachidambaranathan.V (2019), “An overview of designing an induction heating system for domestic applications”, International Journal of Power Electronics and Drive System (IJPEDS) (ISSN 2088-8694) – Vol. 10, No. 1, March 2019, pp. 351 - 356.
 - [49] Swetha and Sivachidambaranathan.V (2019), “A Review on Different Control Techniques Using DSTATCOM For Distribution System Studies” International Journal of Power Electronics and Drive System (IJPEDS) (ISSN 2088-8694) Vol. 10, No. 2, June 2019, pp. 813 – 821.